Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L20	2902	(((integrated adj circuit$1) IC$2 ASIC$2 PIC$2) with fib$3).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:21
L21	49	L20 and (385/14.ccls. 398/140-150, 164.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:23
L22	46	L21 and (@ad<"20030718" @prad<"20030718")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:23
L23	1846	(((integrated adj circuit$1) IC$2 ASIC$2 HIC$2 OEIC$2 OIC$2) with fib$3).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:23
L24	2923	(((integrated adj circuit$1) IC$2 ASIC$2 PIC$2 HIC$2 OEIC$2 OIC$2) with fib$3).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:25
L25	49	L24 and (385/14.ccls. 398/140-150, 164.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:23
L26	46	L25 and (@ad<"20030718" @prad<"20030718")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:23
L27	24119	(((integrated adj circuit$1) IC$2 ASIC$2 PIC$2 HIC$2 OEIC$2 OIC$2) with wir$3).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:25

L28	1527	((((integrated adj circuit$1) IC$2 ASIC$2 PIC$2 HIC$2 OEIC$2 OIC$2) with wir$3) and ((transmis$4 transmit$4 send$4 receiv$4 detect$4 communicat$4) near7 (data signal$1))).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:27
L29	38	((((integrated adj circuit$1) IC$2 ASIC$2 PIC$2 HIC$2 OEIC$2 OIC$2) with wir$3) and ((transmis$4 transmit$4 send$4 receiv$4 detect$4 communicat$4) near7 (data signal$1)) and ((within on) near7 (chip IC circuit))).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 15:28
L30	1	("6233376").PN.	USPAT	OR	OFF	2005/07/10 15:59
L31	1	("5757658").PN.	USPAT	OR	OFF	2005/07/10 15:59
S1	54352	(logic core$1) same (transmi$6) same (receiv$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:17
S2	570009	integrat$3 near3 circuit$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 17:59
S3	5740	optic$4 near3 transmi$6 near3 network$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 18:00
S4	144	((logic core$1) same (transmi$6) same (receiv$3)) and (integrat$3 near3 circuit$2) and (optic$4 near3 transmi$6 near3 network$1) and @ad<"20030718"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 18:26
S5	894	(385/14.ccls.) and (transmi$6 same receiv$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 18:29
S6	570	((385/14.ccls.) and (transmi$6 same receiv$3)) and (data info$7) and (level$1 plane$1 layer$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 18:28
S7	135	(385/14.ccls.) and (transmi$6 same receiv$3 same (data info$7)) and (transmi$6 same receiv$3 same (level$1 layer$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 18:30

S8	130	(385/14.ccls.) and (transmi$6 same receiv$3 same (data info$7)) and (transmi$6 same receiv$3 same (level$1 layer$1)) and @ad<"20030718"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 18:30
S9	26	(US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.	US-PGPUB; USPAT	OR	OFF	2004/10/14 10:55
S10	0	((US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.) and (oxide with glass with layer)	USPAT	OR	OFF	2004/10/14 10:55

S11	0	((US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.) and (oxide with glass)	USPAT	OR	OFF	2004/10/14 10:55
S12	0	((US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.) and (glass with oxide)	US-PGPUB; USPAT	OR	OFF	2004/10/14 10:56

S13	0	((US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.) and (glass$3 with oxide$3)	US-PGPUB; USPAT	OR	OFF	2004/10/14 10:56
S14	1	((US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.) and (glass and oxide)	US-PGPUB; USPAT	OR	OFF	2004/10/14 10:57

S15	11	((US-6624077-$ or US-RE36471-$ or US-5914976-$ or US-5926303-$ or US-4838633-$ or US-6788836-$ or US-6771845-$ or US-6775428-$ or US-6503768-$ or US-6430325-$ or US-5416861-$ or US-5191219-$ or US-5200631-$ or US-5138475-$ or US-5061027-$).did. or (US-20040005151-$ or US-20030114006-$ or US-20030026515-$ or US-20020131727-$ or US-20020047646-$ or US-20020003640-$ or US-20020035640-$ or US-20040047539-$ or US-20040042705-$ or US-20030179978-$ or US-20030026517-$).did.) and (glass)	US-PGPUB; USPAT	OR	OFF	2004/10/14 11:23
S16	6	(("6410941") or ("6169833") or ("6150188") or ("5502785") or ("5357593") or ("4744623")).PN.	USPAT; USOCR	OR	OFF	2004/10/14 15:57
S17	2	(("20020031319") or ("20020191916")).PN.	US-PGPUB; USOCR	OR	OFF	2004/10/14 15:57
S18	8	((("6410941") or ("6169833") or ("6150188") or ("5502785") or ("5357593") or ("4744623")).PN.) or ((("20020031319") or ("20020191916")).PN.)	US-PGPUB; USPAT	OR	OFF	2004/10/14 17:20
S19	17928	((silicon adj (dioxide oxide)) silicon-dioxide silicon-oxide) with glass	US-PGPUB; USPAT	OR	OFF	2004/10/14 17:23
S20	74	(((silicon adj (dioxide oxide)) silicon-dioxide silicon-oxide) with glass) and 385/14.ccls.	US-PGPUB; USPAT	OR	OFF	2004/10/14 17:22
S21	74	(((silicon adj (dioxide oxide)) silicon-dioxide silicon-oxide) with glass) and 385/14.ccls. and @ad<"20030718"	US-PGPUB; USPAT	OR	OFF	2004/10/14 17:22
S22	47	((((silicon adj (dioxide oxide)) silicon-dioxide silicon-oxide) with glass) with (substrate layer channel plane)) and 385/14.ccls. and @ad<"20030718"	US-PGPUB; USPAT	OR	OFF	2004/10/14 17:24
S27	2414323	(integrat$4 near3 circuit$1) IC$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 07:50

S28	24826	(transmi$6 with (optic$4 near3 fib$3)) same (receiv$5 with (optic$4 near fib$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 07:54
S29	74445	transmi$6 same receiv$5 same (core$1 logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 07:55
S30	960	S27 and S28 and S29	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 07:56
S31	840	S30 and @ad<"20030718"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 07:57
S32	117	S31 and (fib$3 near5 (layer$1 mat$1 level$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 07:58
S33	1	"651317".apn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/05 10:52
S34	54916	((integrated adj circuit$2) IC$2) same (optic$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:20
S35	190694	(core$1 transmitter$1 receiver$1 logic) with (optic$4 fib$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:19
S36	8985	S35 and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:19

S37	65197	((integrated adj circuit$2) IC$2) same (optic$4 fib$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:20
S38	9981	S37 and S35	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:20
S39	6079	((integrated adj circuit$2) IC$2) same (optic$4 near3 fib$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:20
S40	2492	S39 and S35	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:21
S41	1265	S40 and ("398"/$.ccls. "385"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:22
S42	283	S41 and (385/14.ccls. 398/140-150, 164.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:25
S43	257	S42 and (@ad<"20030718" @prad<"20030718")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 14:25

S44	43	(US-20020003640-$ or US-20020035640-$ or US-20020047646-$ or US-20020131727-$ or US-20030026515-$ or US-20030026517-$ or US-20030114006-$ or US-20030152309-$ or US-20030179978-$ or US-20040005151-$ or US-20040042705-$ or US-20040047539-$ or US-20040017962-$ or US-20030007745-$ or US-20020085784-$ or US-20010031109-$).did. or (US-4838633-$ or US-5061027-$ or US-5138475-$ or US-5191219-$ or US-5200631-$ or US-5416861-$ or US-5914976-$ or US-5926303-$ or US-6430325-$ or US-6503768-$ or US-6624077-$ or US-6771845-$ or US-6775428-$ or US-6788836-$ or US-6869229-$ or US-RE36471-$ or US-6693736-$ or US-6393169-$ or US-6215585-$ or US-5848214-$ or US-5835646-$ or US-5652811-$ or US-5313535-$ or US-5235663-$ or US-5208879-$ or US-5148504-$). did. or (US-5123078-$).did.	US-PGPUB; USPAT	OR	OFF	2005/07/09 15:00
S45	28	("5148504").URPN.	USPAT	OR	OFF	2005/07/09 16:28
S46	3109	(((integrated adj circuit$3) IC$2) same (fib$3)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 17:03
S47	75	S46 and (385/14.ccls. 398/140-150, 164.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 17:04
S48	71	S47 and (@ad<"20030718" @prad<"20030718")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 17:04
S49	8	(("6410941") or ("6169833") or ("6150188") or ("5502785") or ("5357593") or ("4744623") or ("20020031319") or ("20020191916")).PN.	US-PGPUB; USPAT	OR	OFF	2005/07/09 18:20

S50	90388	((integrated adj circuit$2) ASIC$2 IC$2) same (transmit$4 and receiv$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 18:22
S51	15583	S50 and optic$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 18:21
S52	6933	(((integrated adj circuit$2) ASIC$2 IC$2) same (transmit$4 and receiv$4)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 18:22
S53	579	S52 and optic$4.ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 18:22
S54	39	S53 and (385/14.ccls. 398/140-150, 164.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/09 18:23
S55	33	S54 and (@ad<"20030718" @prad<"20030718")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 10:41
S56	1	("4274104").PN.	USPAT	OR	OFF	2005/07/10 11:42
S57	880	((integrated adj circuit$1) IC$2 ASIC$2) same ((transmission communication$1) near3 (system$1 network$1)) same (core$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:44
S58	418	S57 and (core$1 same ((transmission transmit$4 send$4) and (receiv$4 detect$4)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:55

S59	84	S58 and (fib$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:46
S60	1584	(transmit$4 transmission send$4) with (signal$1) with ((within on internal$4) near5 (chip IC (integrated adj circuit)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:56
S61	2266	(transmit$4 transmission send$4) with (data information signal$1) with ((within on internal$4) near5 (chip IC (integrated adj circuit)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:58
S62	3	S61 and S58	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:57
S63	231	(transmit$4 transmission send$4) with (data information signal$1) with ((within on internal$4) near5 (chip IC (integrated adj circuit)) with (path fib$4 waveguide$1 trace$1 wir$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 11:59
S64	209	S63 and (@ad<"20030718" @prad<"20030718")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/10 12:00

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Date: 7/10/2005
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Inventor Name Search Result

Your Search was:

Last Name = DOYLE
First Name = GARY

Application#	Patent#	Status	Date Filed	Title	Inventor Name 5
10604410	Not Issued	071	07/18/2003	FIBER OPTIC TRANSMISSION LINES ON AN SOC	DOYLE, GARY R.
09644581	6357927	150	08/24/2000	END PLAY PRELOAD ADJUSTING ASSEMBLY FOR BEARING	DOYLE, GARY PETER
09387062	6305004	150	08/31/1999	METHOD FOR IMPROVING WIRING RELATED YIELD AND CAPACITANCE PROPERTIES OF INTEGRATED CIRCUITS BY MAZE-ROUTING	DOYLE, GARY
08620720	5663637	150	03/19/1996	ROTARY SIGNAL COUPLING FOR CHEMICAL MECHANICAL POLISHING ENDPOINT DETECTION WITH A WESTECH TOOL	DOYLE, GARY R.

Inventor Search Completed: No Records to Display.

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Last Name = GOODNOW
First Name = KENNETH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 47
11160609	Not Issued	020	06/30/2005	APPARATUS AND METHOD FOR IMPLEMENTING AN INTEGRATED CIRCUIT IP CORE LIBRARY ARCHITECTURE	GOODNOW, KENNETH J.
11160601	Not Issued	020	06/30/2005	METHOD AND APPARATUS FOR MONITORING INTEGRATED CIRCUIT TEMPERATURE THROUGH DETERMINISTIC PATH DELAYS	GOODNOW, KENNETH J.
10908597	Not Issued	030	05/18/2005	A METHOD AND APPARATUS FOR TRANSFERRING DATA BETWEEN CORES IN AN INTEGRATED CIRCUIT	GOODNOW, KENNETH J.
10906476	Not Issued	020	02/22/2005	METHOD OF SWITCHING VOLTAGE ISLANDS IN INTEGRATED CIRCUITS	GOODNOW, KENNETH J.
10906388	Not Issued	020	02/17/2005	SYSTEM AND METHOD FOR SYSTEM-ON-CHIP INTERCONNECT VERIFICATION	GOODNOW, KENNETH J.
10904259	Not Issued	020	11/01/2004	METHOD AND APPARATUS FOR SERVICING THREADS WITHIN A MULTI-PROCESSOR SYSTEM	GOODNOW, KENNETH J.
10729750	Not Issued	041	12/04/2003	DATA PROCESSING IN DIGITAL SYSTEMS	GOODNOW, KENNETH J.
10725712	Not Issued	040	12/02/2003	METHOD FOR MODIFYING THE BEHAVIOR OF A STATE MACHINE	GOODNOW, KENNETH J.
10711084	Not Issued	030	08/20/2004	SYSTEM AND METHOD FOR ARBITRATION BETWEEN SHARED PERIPHERAL CORE DEVICES IN SYSTEM ON CHIP ARCHITECTURES	GOODNOW, KENNETH J.
10711082	Not Issued	030	08/20/2004	COMMUNICATION SYSTEMS AND METHODS USING MICROELECTRONICS POWER DISTRIBUTION NETWORK	GOODNOW, KENNETH J.
10709809	Not Issued	030	05/28/2004	METHOD FOR SYSTEM LEVEL PROTECTION OF FIELD PROGRAMMABLE LOGIC DEVICES	GOODNOW, KENNETH J.
10707323	Not Issued	030	12/05/2003	METHOD OF SELECTIVELY BUILDING REDUNDANT LOGIC STRUCTURES TO IMPROVE FAULT TOLERANCE	GOODNOW, KENNETH J.
10707304	Not Issued	030	12/04/2003	MULTIPROCESSOR CODE FIX USING A LOCAL CACHE	GOODNOW, KENNETH J.
10692193	Not Issued	020	10/23/2003	METHOD AND STRUCTURE FOR REPLACING FAULTY OPERATING CODE CONTAINED IN A ROM FOR A PROCESSOR	GOODNOW, KENNETH J.
10605603	Not Issued	092	10/13/2003	SYSTEM AND METHOD FOR DYNAMICALLY EXECUTING A FUNCTION IN A PROGRAMMABLE LOGIC ARRAY	GOODNOW, KENNETH J.
10605366	Not Issued	030	09/25/2003	SEMICONDUCTOR DEVICE COMPRISING A PLURALITY OF MEMORY STRUCTURES	GOODNOW, KENNETH J.
10604410	Not Issued	071	07/18/2003	FIBER OPTIC TRANSMISSION LINES ON AN SOC	GOODNOW, KENNETH J.

10604328	Not Issued	020	07/11/2003	POWER DOWN PROCESSING ISLANDS	GOODNOW, KENNETH J.
10249684	6825711	150	04/30/2003	POWER REDUCTION BY STAGE IN INTEGRATED CIRCUIT	GOODNOW, KENNETH J.
10249568	Not Issued	030	04/19/2003	WIRELESS COMMUNICATION SYSTEM WITHIN A SYSTEM ON A CHIP	GOODNOW, KENNETH J.
10248527	Not Issued	041	01/27/2003	DUAL TIME SLICED CIRCULAR BUS	GOODNOW, KENNETH J.
10064582	Not Issued	061	07/29/2002	SYSTEM AND METHOD FOR CORRECTING TIMING SIGNALS IN INTEGRATED CIRCUITS	GOODNOW, KENNETH J.
09777365	Not Issued	161	02/05/2001	DIRECTED LEAST RECENTLY USED CACHE REPLACEMENT METHOD	GOODNOW, KENNETH J.
09709872	6604174	150	11/10/2000	PERFORMANCE BASED SYSTEM AND METHOD FOR DYNAMIC ALLOCATION OF A UNIFIED MULTIPORT CACHE	GOODNOW, KENNETH J.
09682835	6541997	150	10/23/2001	CLOCKLESS IMPEDANCE CONTROLLER	GOODNOW, KENNETH J.
09492624	Not Issued	161	01/27/2000	METHOD FOR IP CORE PROTECTION IN ASIC DESIGNS	GOODNOW, KENNETH J.
09394965	6532520	150	09/10/1999	METHOD AND APPARATUS FOR ALLOCATING DATA AND INSTRUCTIONS WITHIN A SHARED CACHE	GOODNOW, KENNETH J.
09286855	6802033	150	04/06/1999	LOW-POWER CRITICAL ERROR RATE COMMUNICATIONS CONTROLLER	GOODNOW, KENNETH J.
09275170	6317840	150	03/24/1999	CONTROL OF MULTIPLE EQUIVALENT FUNCTIONAL UNITS FOR POWER REDUCTION	GOODNOW, KENNETH J.
09159898	6097241	150	09/24/1998	ASIC LOW POWER ACTIVITY DETECTOR TO CHANGE THRESHOLD VOLTAGE	GOODNOW, KENNETH J.
09159861	6097243	150	09/24/1998	DEVICE AND METHOD TO REDUCE POWER CONSUMPTION IN INTEGRATED SEMICONDUCTOR DEVICES USING A LOW POWER GROGGY MODE	GOODNOW, KENNETH J.
09149686	6107841	150	09/08/1998	SYNCHRONOUS CLOCK SWITCHING CIRCUIT FOR MULTIPLE ASYNCHRONOUS CLOCK SOURCE	GOODNOW, KENNETH J.
09136126	6237132	150	08/18/1998	TOGGLE BASED APPLICATION SPECIFIC CORE METHODOLOGY	GOODNOW, KENNETH J.
09135825	6397170	150	08/18/1998	SIMULATION BASED POWER OPTIMIZATION	GOODNOW, KENNETH J.
09129921	6275968	150	08/06/1998	APPARATUS AND METHOD TO REDUCE NODE TOGGLING IN SEMICONDUCTOR DEVICES	GOODNOW, KENNETH J.
09120211	6011383	150	07/21/1998	LOW POWERING APPARATUS FOR AUTOMATIC REDUCTION OF POWER IN ACTIVE AND STANDBY MODES	GOODNOW, KENNETH J.
09103383	Not Issued	161	06/24/1998	LOW POWER STAND-BY MODE USING COMPRESSED DATA AND METHOD FOR USING SAME	GOODNOW, KENNETH J
09074442	6081135	150	05/07/1998	DEVICE AND METHOD TO REDUCE POWER CONSUMPTION IN INTEGRATED SEMICONDUCTOR DEVICES	GOODNOW, KENNETH J.
09073999	6658634	150	05/07/1998	LOGIC POWER OPTIMIZATION ALGORITHM	GOODNOW, KENNETH
09056300	6167524	150	04/06/1998	APPARATUS AND METHOD FOR EFFICIENT BATTERY UTILIZATION IN PORTABLE PERSONAL COMPUTERS	GOODNOW, KENNETH J.

09054459	6134704	150	04/03/1998	INTEGRATED CIRCUIT MACRO APPARATUS	GOODNOW, KENNETH J.
08835126	Not Issued	161	04/04/1997	PREDICTIVE CACHE LOADING BY PROGRAM ADDRESS DISCONTINUITY HISTORY	GOODNOW, KENNETH J.
08770355	6141351	150	12/20/1996	RADIO FREQUENCY BUS FOR BROADBAND MICROPROCESSOR COMMUNICATIONS	GOODNOW, KENNETH J.
08751468	6026471	150	11/19/1996	ANTICIPATING CACHE MEMORY LOADER AND METHOD	GOODNOW, KENNETH J.
08751465	5781922	150	11/19/1996	PAGE BOUNDARY CACHES	GOODNOW, KENNETH J.
08504347	5710892	150	07/19/1995	SYSTEM AND METHOD FOR ASYNCHRONOUS DUAL BUS CONVERSION USING DOUBLE STATE MACHINES	GOODNOW, KENNETH J.

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Your Search was:

Last Name = HARDING
First Name = RIYON

Application#	Patent#	Status	Date Filed	Title	Inventor Name 7
10707304	Not Issued	030	12/04/2003	MULTIPROCESSOR CODE FIX USING A LOCAL CACHE	HARDING, RIYON W.
10604410	Not Issued	071	07/18/2003	FIBER OPTIC TRANSMISSION LINES ON AN SOC	HARDING, RIYON W.
10604178	Not Issued	030	06/30/2003	RANDOM NUMBER GENERATOR	HARDING, RIYON W.
10249568	Not Issued	030	04/19/2003	WIRELESS COMMUNICATION SYSTEM WITHIN A SYSTEM ON A CHIP	HARDING, RIYON W.
10248527	Not Issued	041	01/27/2003	DUAL TIME SLICED CIRCULAR BUS	HARDING, RIYON
09682835	6541997	150	10/23/2001	CLOCKLESS IMPEDANCE CONTROLLER	HARDING, RIYON

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Your Search was:

Last Name = KAMPF
First Name = FRANCIS

Application#	Patent#	Status	Date Filed	Title	Inventor Name 19
10904056	Not Issued	030	10/21/2004	SIMULATION TESTING OF DIGITAL LOGIC CIRCUIT DESIGNS	KAMPF, FRANCIS A.
10605366	Not Issued	030	09/25/2003	SEMICONDUCTOR DEVICE COMPRISING A PLURALITY OF MEMORY STRUCTURES	KAMPF, FRANCIS A.
10604410	Not Issued	071	07/18/2003	FIBER OPTIC TRANSMISSION LINES ON AN SOC	KAMPF, FRANCIS A.
10064582	Not Issued	061	07/29/2002	SYSTEM AND METHOD FOR CORRECTING TIMING SIGNALS IN INTEGRATED CIRCUITS	KAMPF, FRANCIS A.
10063456	Not Issued	030	04/24/2002	RECONFIGURABLE CIRCULAR BUS	KAMPF, FRANCIS A.
10063107	6657565	150	03/21/2002	METHOD AND SYSTEM FOR IMPROVING LOSSLESS COMPRESSION EFFICIENCY	KAMPF, FRANCIS A.
09379864	6281816	150	08/24/1999	METHOD AND APPARATUS FOR REDUCING DATA EXPANSION DURING DATA COMPRESSION	KAMPF, FRANCIS A.
09379821	6271775	150	08/24/1999	METHOD FOR REDUCING DATA EXPANSION DURING DATA COMPRESSION	KAMPF, FRANCIS A.
08999177	6337852	150	12/29/1997	A FLOW CONTROL SYSTEM USING CONTROL INFORMATION OF A MESSAGE FOR INITIATING RETRANSMISSION OF DATA PORTION WHEN BUFFER IS AVAILABLE	KAMPF, FRANCIS A.
08998966	6338091	150	12/29/1997	SYSTEM FOR OPTIMISTIC TRANSMISSION FLOW CONTROL INCLUDING RECEIVER DATA DISCARDS UPON INADEQUATE BUFFERING CONDITION	KAMPF, FRANCIS A.
08998965	6480897	150	12/29/1997	OPTIMISTIC TRANSMISSION FLOW CONTROL INCLUDING RECEIVER DATA DISCARDS UPON INADEQUATE BUFFERING CONDITION	KAMPF, FRANCIS A.
08838389	5968189	150	04/08/1997	SYSTEM OF REPORTING ERRORS BY A HARDWARE ELEMENT OF A DISTRIBUTED COMPUTER SYSTEM	KAMPF, FRANCIS ALFRED
08835434	6098105	150	04/08/1997	SOURCE AND DESTINATION INITIATED INTERRUPT METHOD FOR MESSAGE ARRIVAL NOTIFICATION	KAMPF, FRANCIS A.
08831540	6003091	150	04/08/1997	VERIFYING A TIME-OF-DAY COUNTER	KAMPF, FRANCIS ALFRED
08831255	5923840	150	04/08/1997	METHOD OF REPORTING ERRORS BY A HARDWARE ELEMENT OF A DISTRIBUTED COMPUTER SYSTEM	KAMPF, FRANCIS ALFRED
08826888	6105071	150	04/08/1997	SOURCE AND DESTINATION INITIATED INTERRUPT SYSTEM FOR MESSAGE ARRIVAL NOTIFICATION	KAMPF, FRANCIS A.
08825793	5925107	150	04/08/1997	VERIFYING A TIME-OF-DAY COUNTER	KAMPF, FRANCIS A.
08824697	6098104	150	04/08/1997	SOURCE AND DESTINATION INITIATED	KAMPF, FRANCIS A.

| | | | | INTERRUPTS FOR MESSAGE ARRIVAL NOTIFICATION, AND RELATED DATA STRUCTURES | | |

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KAMPF	FRANCIS	Search

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Your Search was:

Last Name = NORMAN
First Name = JASON

Application#	Patent#	Status	Date Filed	Title	Inventor Name 15
11160601	Not Issued	020	06/30/2005	METHOD AND APPARATUS FOR MONITORING INTEGRATED CIRCUIT TEMPERATURE THROUGH DETERMINISTIC PATH DELAYS	NORMAN, JASON M.
10908597	Not Issued	030	05/18/2005	A METHOD AND APPARATUS FOR TRANSFERRING DATA BETWEEN CORES IN AN INTEGRATED CIRCUIT	NORMAN, JASON M.
10906388	Not Issued	020	02/17/2005	SYSTEM AND METHOD FOR SYSTEM-ON-CHIP INTERCONNECT VERIFICATION	NORMAN, JASON M.
10904259	Not Issued	020	11/01/2004	METHOD AND APPARATUS FOR SERVICING THREADS WITHIN A MULTI-PROCESSOR SYSTEM	NORMAN, JASON M.
10711084	Not Issued	030	08/20/2004	SYSTEM AND METHOD FOR ARBITRATION BETWEEN SHARED PERIPHERAL CORE DEVICES IN SYSTEM ON CHIP ARCHITECTURES	NORMAN, JASON M.
10680756	Not Issued	030	10/07/2003	DATA ACKNOWLEDGMENT USING IMPEDANCE MISMATCHING	NORMAN, JASON M.
10605884	Not Issued	094	11/04/2003	AUTO-LINKING OF FUNCTION LOGIC STATE WITH TESTCASE REGRESSION LIST	NORMAN, JASON MICHAEL
10605366	Not Issued	030	09/25/2003	SEMICONDUCTOR DEVICE COMPRISING A PLURALITY OF MEMORY STRUCTURES	NORMAN, JASON M.
10604410	Not Issued	071	07/18/2003	FIBER OPTIC TRANSMISSION LINES ON AN SOC	NORMAN, JASON M.
10505621	Not Issued	030	04/08/2005	PARTIAL OXIDATION OF HYDROGEN SULPHIDE	NORMAN, JASON SCOTT
10249568	Not Issued	030	04/19/2003	WIRELESS COMMUNICATION SYSTEM WITHIN A SYSTEM ON A CHIP	NORMAN, JASON M.
10168787	Not Issued	060	11/04/2002	PARTIAL OXIDATION OF HYDROGEN SULPHIDE CONTAINING GAS	NORMAN, JASON SCOTT
10168580	6638057	150	11/04/2002	PARTIAL OXIDATION OF HYDROGEN SULPHIDE	NORMAN, JASON SCOTT
10064582	Not Issued	061	07/29/2002	SYSTEM AND METHOD FOR CORRECTING TIMING SIGNALS IN INTEGRATED CIRCUITS	NORMAN, JASON M.

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Your Search was:

Last Name = VENTRONE
First Name = SEBASTIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 45
10908597	Not Issued	030	05/18/2005	A METHOD AND APPARATUS FOR TRANSFERRING DATA BETWEEN CORES IN AN INTEGRATED CIRCUIT	VENTRONE, SEBASTIAN T.
10906476	Not Issued	020	02/22/2005	METHOD OF SWITCHING VOLTAGE ISLANDS IN INTEGRATED CIRCUITS	VENTRONE, SEBASTIAN T.
10906343	Not Issued	020	02/15/2005	SYSTEM AND METHOD FOR BALANCING DELAY OF SIGNAL COMMUNICATION PATHS THROUGH WELL VOLTAGE ADJUSTMENT	VENTRONE, SEBASTIAN T.
10906017	Not Issued	020	01/31/2005	SYSTEM AND METHOD FOR DYNAMICALLY MANAGING POWER CONSUMPTION OF INTEGRATED CIRCUITRY	VENTRONE, SEBASTIAN T.
10904397	Not Issued	030	11/08/2004	METHOD AND APPARATUS FOR CONVERTING GLOBALLY CLOCK-GATED CIRCUITS TO LOCALLY CLOCK-GATED CIRCUITS	VENTRONE, SEBASTIAN T.
10863194	Not Issued	030	06/08/2004	DIGITAL RELIABILITY MONITOR HAVING AUTONOMIC REPAIR AND NOTIFICATION CAPABILITY	VENTRONE, SEBASTIAN T.
10832658	Not Issued	030	04/27/2004	ASYNCHRONOUS PACKET BASED DUAL PORT LINK LIST HEADER AND DATA CREDIT MANAGEMENT STRUCTURE	VENTRONE, SEBASTIAN T.
10729751	Not Issued	030	12/04/2003	DIGITAL RELIABILITY MONITOR HAVING AUTONOMIC REPAIR AND NOTIFICATION CAPABILITY	VENTRONE, SEBASTIAN T.
10729750	Not Issued	041	12/04/2003	DATA PROCESSING IN DIGITAL SYSTEMS	VENTRONE, SEBASTIAN T.
10725712	Not Issued	040	12/02/2003	METHOD FOR MODIFYING THE BEHAVIOR OF A STATE MACHINE	VENTRONE, SEBASTIAN T.
10710745	Not Issued	030	07/30/2004	METHOD AND APPARATUS FOR CONTROLLING COMMON-MODE OUTPUT VOLTAGE IN FULLY DIFFERENTIAL AMPLIFIERS	VENTRONE, SEBASTIAN T.
10707323	Not Issued	030	12/05/2003	METHOD OF SELECTIVELY BUILDING REDUNDANT LOGIC STRUCTURES TO IMPROVE FAULT TOLERANCE	VENTRONE, SEBASTIAN T.
10707068	Not Issued	030	11/19/2003	METHOD FOR DESIGNING AN INTEGRATED CIRCUIT HAVING MULTIPLE VOLTAGE DOMAINS	VENTRONE, SEBASTIAN T.
10680756	Not Issued	030	10/07/2003	DATA ACKNOWLEDGMENT USING IMPEDANCE MISMATCHING	VENTRONE, SEBASTIAN T.
10605884	Not Issued	094	11/04/2003	AUTO-LINKING OF FUNCTION LOGIC STATE WITH TESTCASE REGRESSION LIST	VENTRONE, SEBASTIAN T.
10605603	Not Issued	092	10/13/2003	SYSTEM AND METHOD FOR DYNAMICALLY EXECUTING A FUNCTION IN A PROGRAMMABLE LOGIC ARRAY	VENTRONE, SEBASTIAN T.
10605591	Not	030	10/10/2003	METHOD AND APPARATUS FOR MEMORY	VENTRONE, SEBASTIAN T.

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10605366	Not Issued	030	09/25/2003	SEMICONDUCTOR DEVICE COMPRISING A PLURALITY OF MEMORY STRUCTURES	VENTRONE, SEBASTIAN T.
10604410	Not Issued	071	07/18/2003	FIBER OPTIC TRANSMISSION LINES ON AN SOC	VENTRONE, SEBASTIAN T.
10604328	Not Issued	020	07/11/2003	POWER DOWN PROCESSING ISLANDS	VENTRONE, SEBASTIAN T.
10604279	Not Issued	030	07/08/2003	AUTOMATIC LATCH COMPRESSION/REDUCTION	VENTRONE, SEBASTIAN T.
10604205	Not Issued	071	07/01/2003	CIRCUIT AND METHOD FOR PIPELINED INSERTION	VENTRONE, SEBASTIAN T.
10604174	Not Issued	094	06/30/2003	METHOD, PROGRAM PRODUCT, AND DESIGN TOOL FOR AUTOMATIC TRANSMISSION LINE SELECTION IN APPLICATION SPECIFIC INTEGRATED CIRCUITS	VENTRONE, SEBASTIAN T
10249568	Not Issued	030	04/19/2003	WIRELESS COMMUNICATION SYSTEM WITHIN A SYSTEM ON A CHIP	VENTRONE, SEBASTIAN T.
10248314	Not Issued	041	01/08/2003	A VOLTAGE LEVEL BUS PROTOCOL FOR TRANSFERRING DATA	VENTRONE, SEBASTIAN R
10001686	Not Issued	030	10/23/2001	PERVASIVE PROACTIVE PROJECT PLANNER	VENTRONE, SEBASTIAN T.
09928573	6711719	150	08/13/2001	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION IN VLSI CIRCUIT DESIGNS	VENTRONE, SEBASTIAN T.
09895778	6545521	150	06/29/2001	LOW SKEW, POWER SEQUENCE INDEPENDENT CMOS RECEIVER DEVICE	VENTRONE, SEBASTIAN T.
09852784	6535016	150	05/11/2001	METHOD AND CIRCUIT FOR PROVIDING COPY PROTECTION IN AN APPLICATION-SPECIFIC INTEGRATED CIRCUIT	VENTRONE, SEBASTIAN T.
09832520	6720673	150	04/11/2001	VOLTAGE ISLAND FENCING	VENTRONE, SEBASTIAN T.
09805200	Not Issued	121	03/14/2001	MICROPROCESSOR INCLUDING CONTROLLER FOR REDUCED POWER CONSUMPTION AND METHOD THEREFOR	VENTRONE, SEBASTIAN T.
09805138	Not Issued	161	03/14/2001	MICROPROCESSOR INCLUDING CONTROLLER FOR REDUCED POWER CONSUMPTION AND METHOD THEREFOR	VENTRONE, SEBASTIAN T.
09805137	Not Issued	092	03/14/2001	A METHOD OF UPDATING A SEMICONDUCTOR DESIGN	VENTRONE, SEBASTIAN T.
09713829	6792582	150	11/15/2000	CONCURRENT LOGICAL AND PHYSICAL CONSTRUCTION OF VOLTAGE ISLANDS FOR MIXED SUPPLY VOLTAGE DESIGNS	VENTRONE, SEBASTIAN T.
09683778	Not Issued	041	02/13/2002	HUB/ROUTER FOR COMMUNICATION BETWEEN CORES USING CARTESIAN COORDINATES	VENTRONE, SEBASTIAN
09681077	6880074	150	12/22/2000	IN-LINE CODE SUPPRESSION	VENTRONE, SEBASTIAN T.
09641425	6304122	150	08/17/2000	LOW POWER LSSD FLIP FLOPS AND A FLUSHABLE SINGLE CLOCK SPLITTER FOR FLIP FLOPS	VENTRONE, SEBASTIAN T.
09615146	6636995	150	07/13/2000	METHOD OF AUTOMATIC LATCH INSERTION FOR TESTING APPLICATION SPECIFIC INTEGRATED CIRCUITS	VENTRONE, SEBASTIAN T
09524661	6282144	150	03/13/2000	MULTI-PORTED MEMORY WITH ASYNCHRONOUS AND SYNCHRONOUS PROTOCOL	VENTRONE, SEBASTIAN T.

09492624	Not Issued	161	01/27/2000	METHOD FOR IP CORE PROTECTION IN ASIC DESIGNS	VENTRONE, SEBASTIAN T.
09260453	6269468	150	03/02/1999	SPLIT I/O CIRCUIT FOR PERFORMANCE OPTIMIZATION OF DIGITAL CIRCUITS	VENTRONE, SEBASTIAN
09103383	Not Issued	161	06/24/1998	LOW POWER STAND-BY MODE USING COMPRESSED DATA AND METHOD FOR USING SAME	VENTRONE, SEBASTIAN T
08927950	6405234	150	09/11/1997	FULL TIME OPERATING SYSTEM	VENTRONE, SEBASTIAN
07796194	5357617	150	11/22/1991	METHOD AND APPARATUS FOR SUBSTANTIALLY CONCURRENT MULTIPLE INSTRUCTION THREAD PROCESSING BY A SINGLE PIPELINE PROCESSOR	VENTRONE, SEBASTIAN

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1.	pub-date > 1994 and TITLE-ABSTR-KEY(((integrated w/2 circuit) OR IC OR OIC) w/25 (fiber OR fibers OR fibre OR fibres)) *[All Sources(- All Sciences -)]*	81